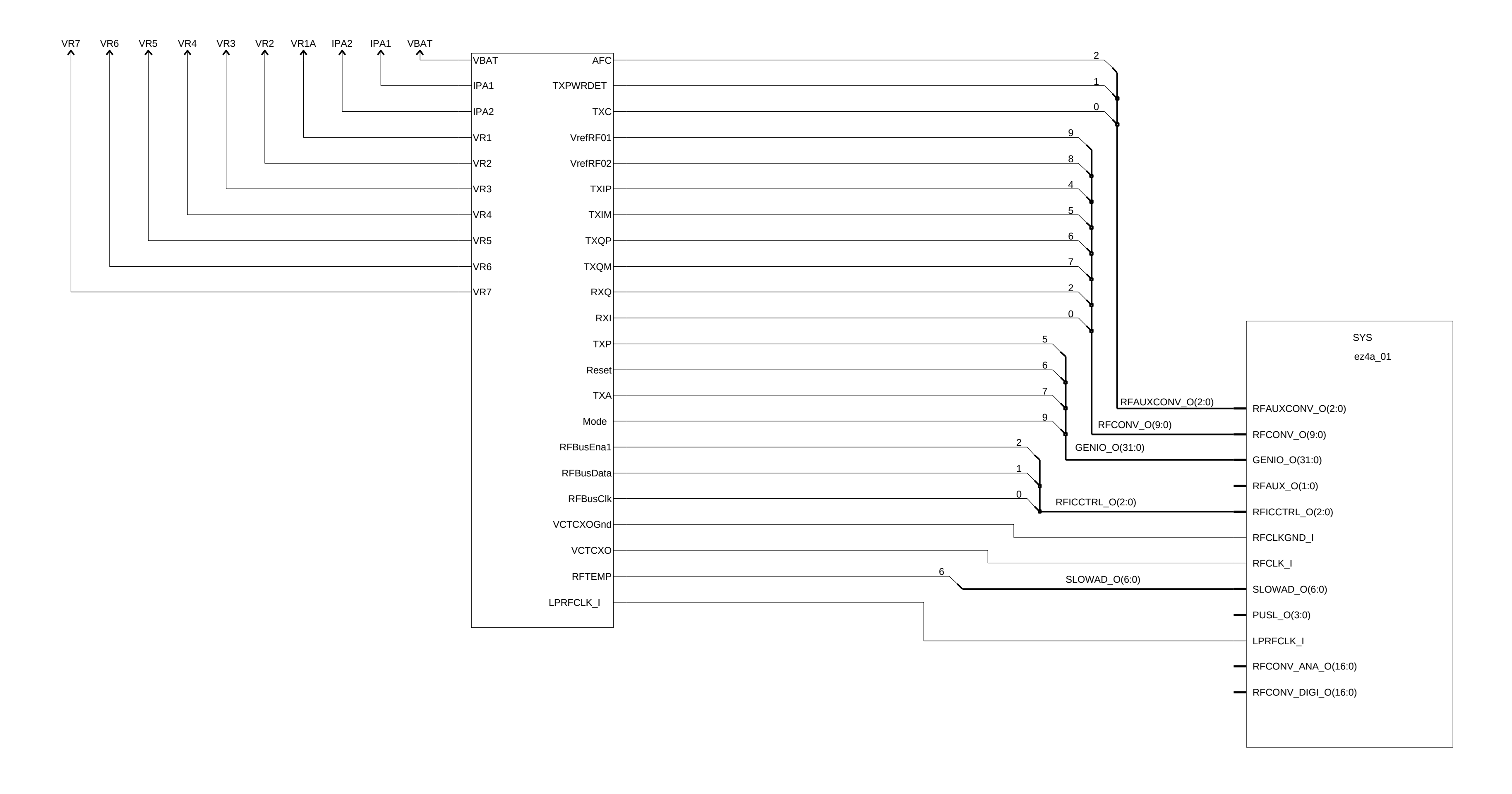
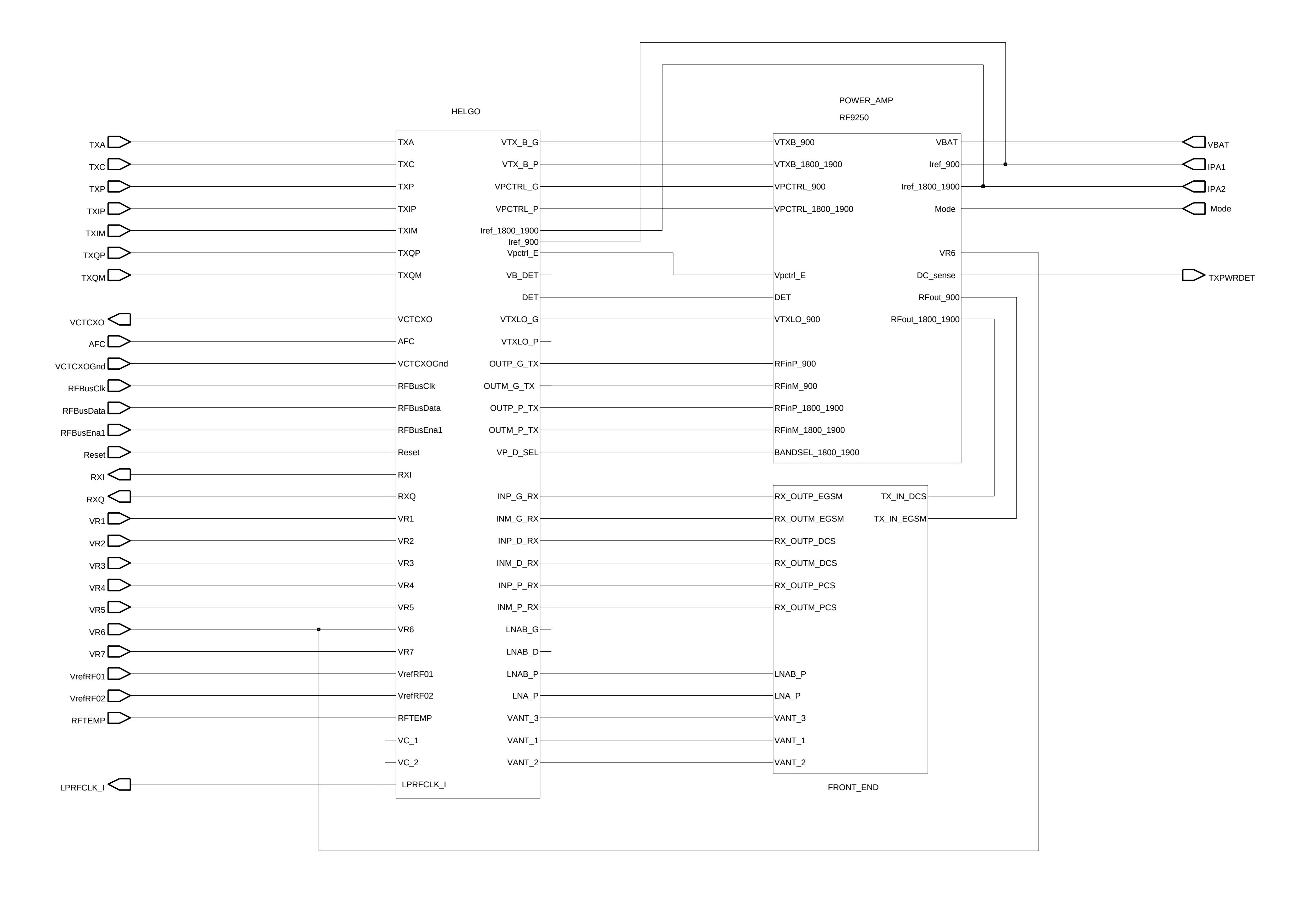
RM-8/RM-47 top level

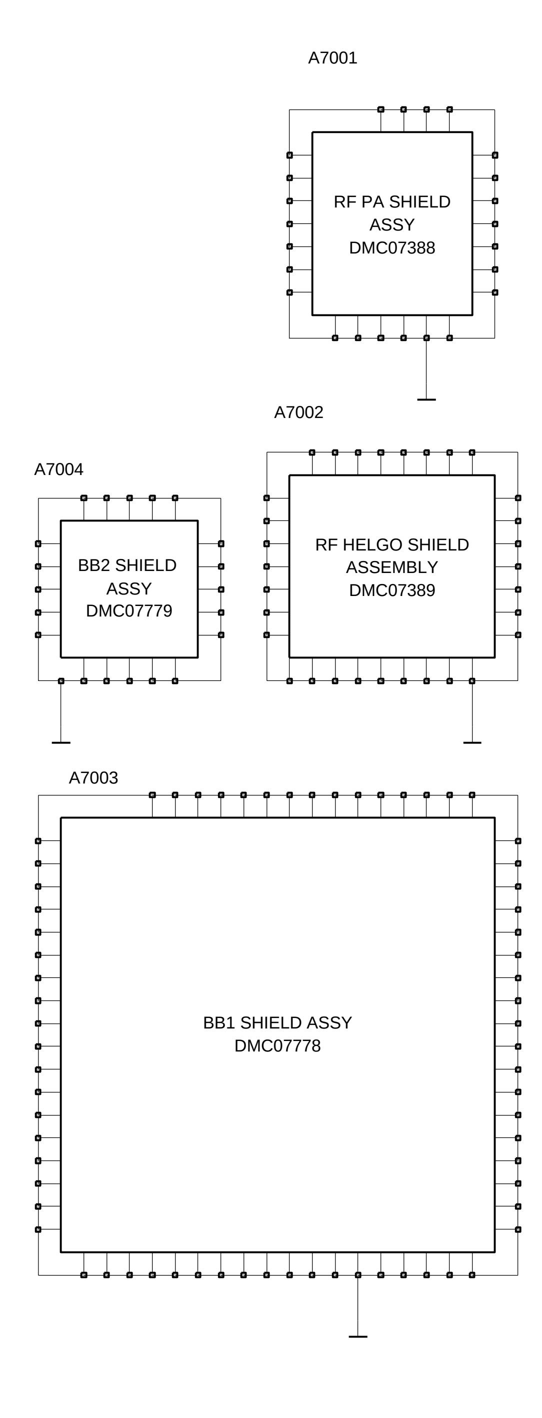


RF top level

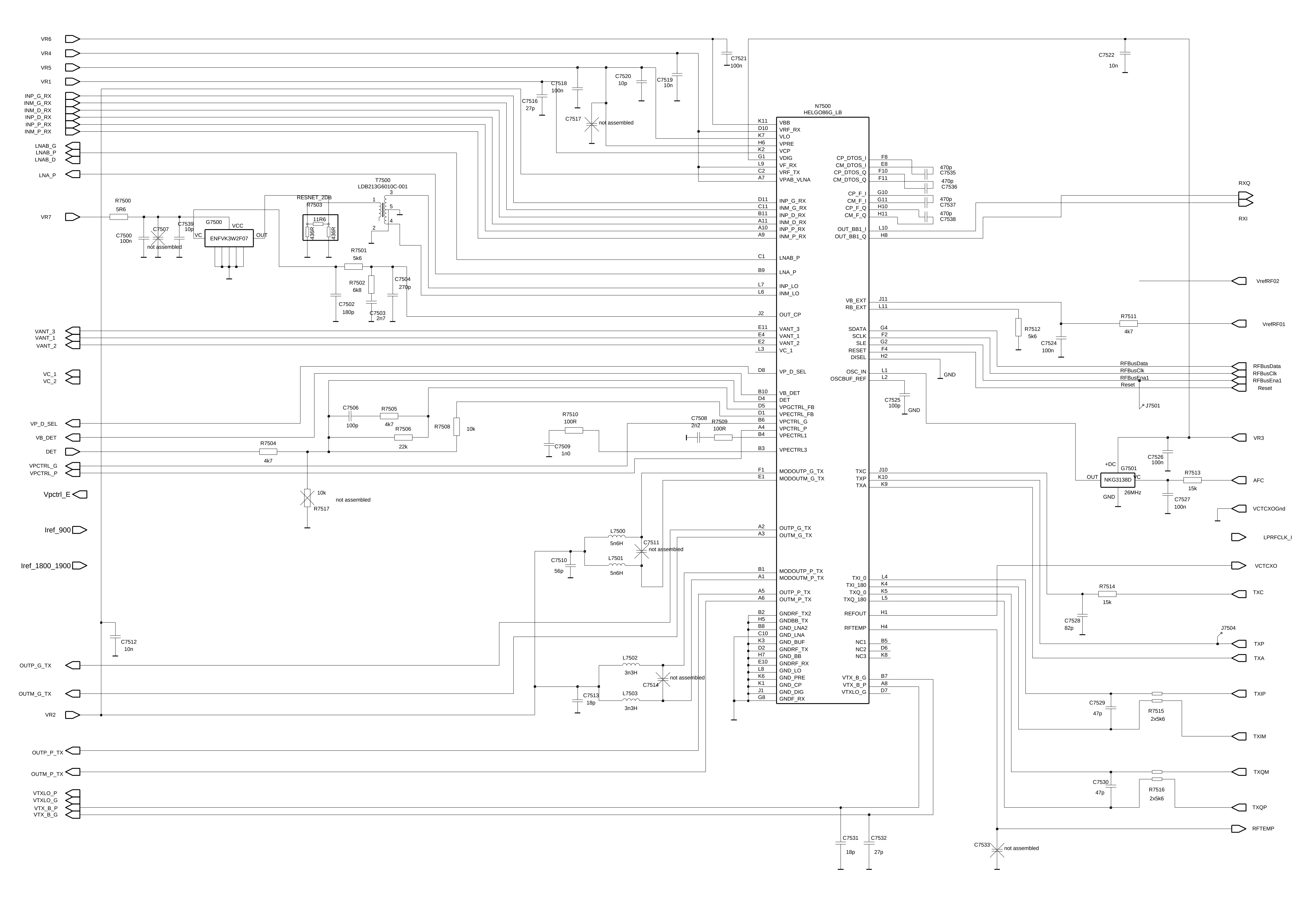


HELGO
POWER_AMP
RF9250
FRONT_END
TXA
TXC
TXP
TXIP
TXIM
TXQP
TXQM
VCTCXO
AFC
VCTCXOGnd
RFBusClk
RFBusData
RFBusEna1
Reset
RXI
RXQ
VR1
VR2
VR3
VR4
VR5
VR6
VR7
VrefRF01
VrefRF02
RFTEMP
VC_1
VC_2
LPRFCLK_I
VTX_B_G
VTX_B_P
VPCTRL_G
VPCTRL_P
Iref_1800_1900
Iref_900
Vpctrl_E
VB_DET
DET
VTXLO_G
VTXLO_P
OUTP_G_TX
OUTM_G_TX
OUTP_P_TX
OUTM_P_TX
VP_D_SEL
INP_G_RX
INM_G_RX
INP_D_RX
INM_D_RX
INP_P_RX
INM_P_RX
LNAB_G
LNAB_D
LNAB_P
LNA_P
VANT_3
VANT_1
VANT_2
VTXB_900
VTXB_1800_1900
VPCTRL_900
VPCTRL_1800_1900
Vpctrl_E
DET
VTXLO_900
RFinP_900
RFinM_900
RFinP_1800_1900
RFinM_1800_1900
BANDSEL_1800_1900
VBAT
Iref_900
Iref_1800_1900
Mode
VR6
DC_sense
RFout_900
RFout_1800_1900
RX_OUTP_EGSM
RX_OUTM_EGSM
RX_OUTP_DCS
RX_OUTM_DCS
RX_OUTP_PCS
RX_OUTM_PCS
TX_IN_DCS
TX_IN_EGSM
IPA1
IPA2
TXPWRDET

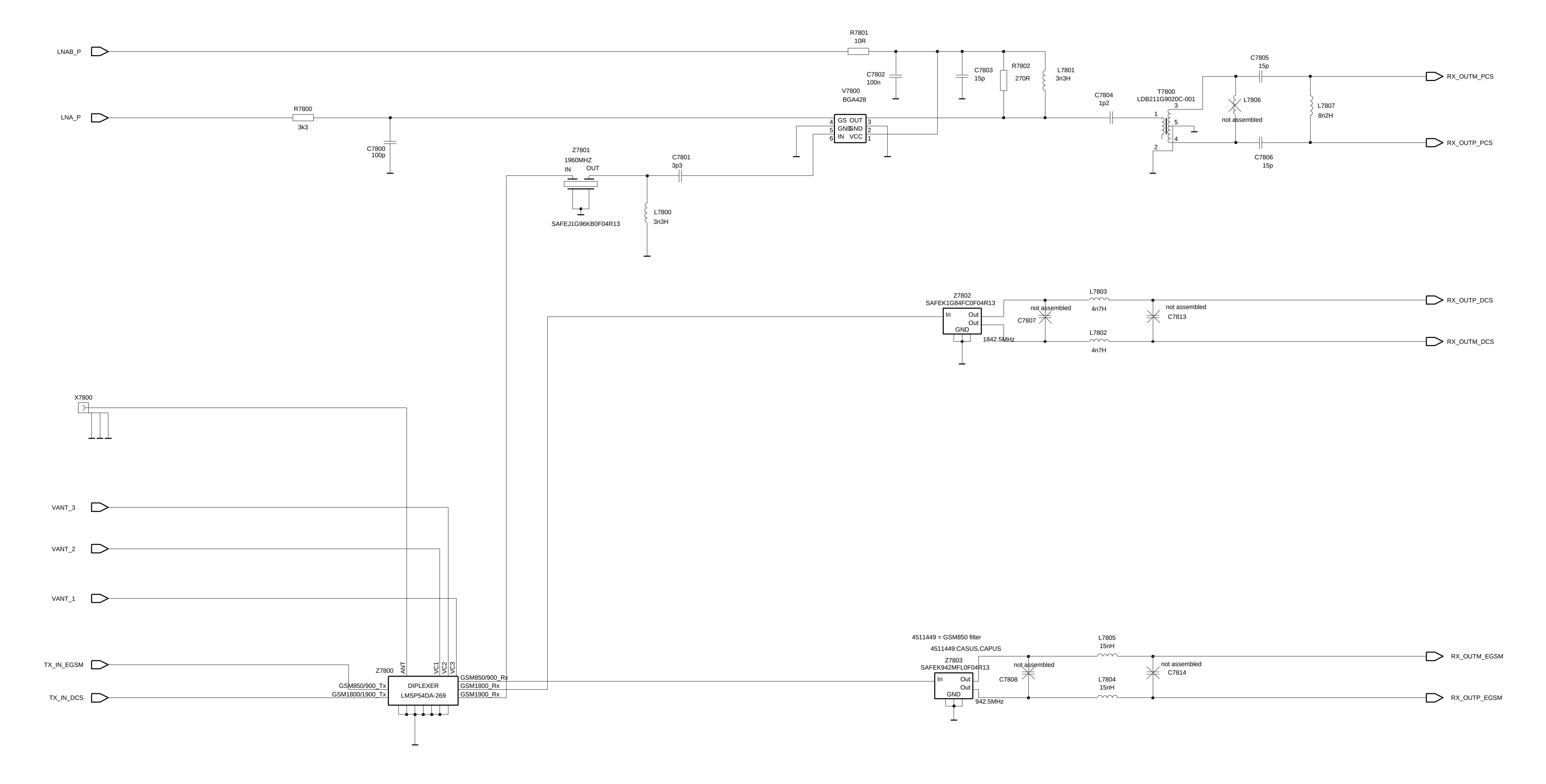
RF shields and vias



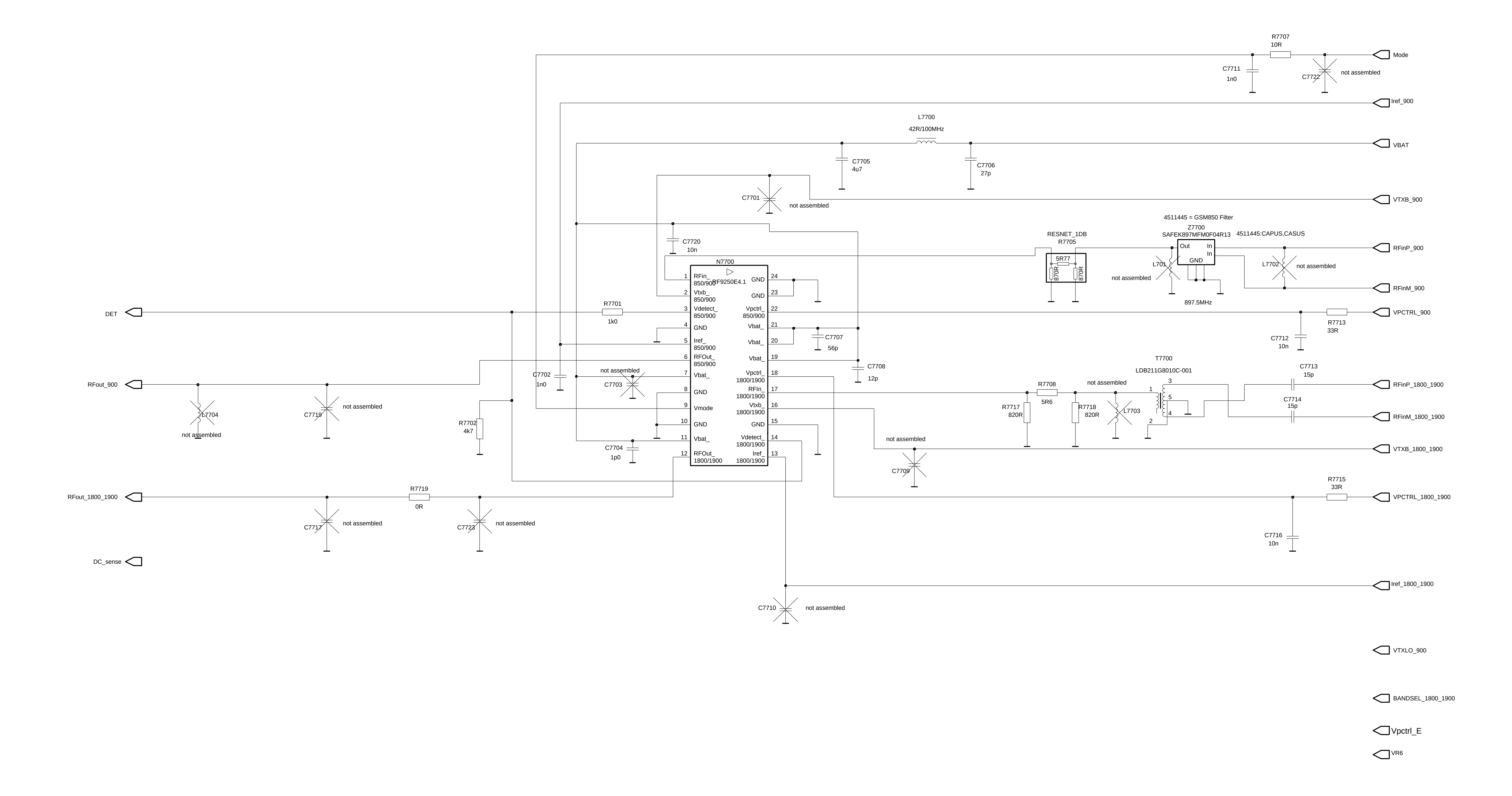
HELGO



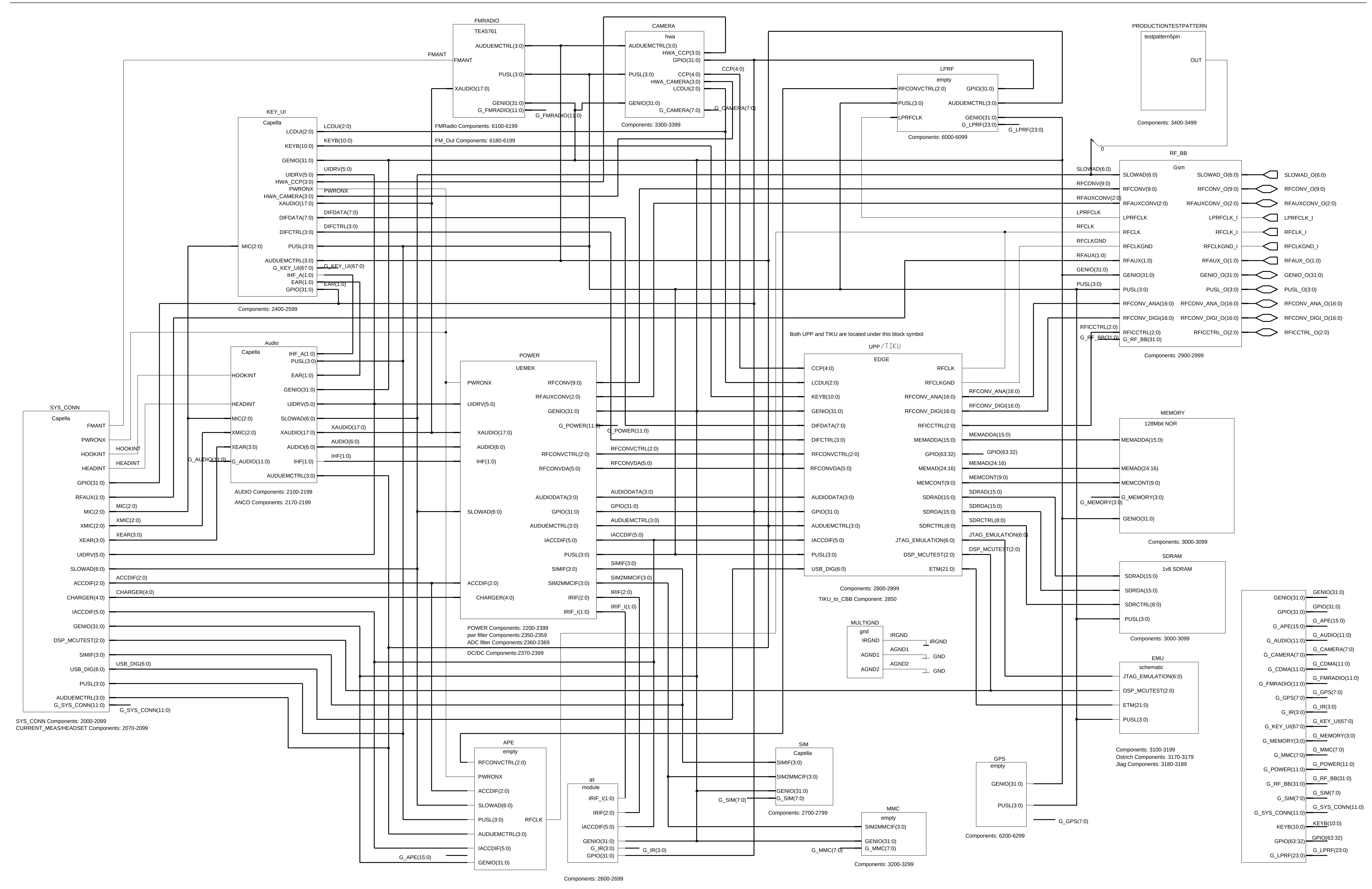
RX front end and antenna switch



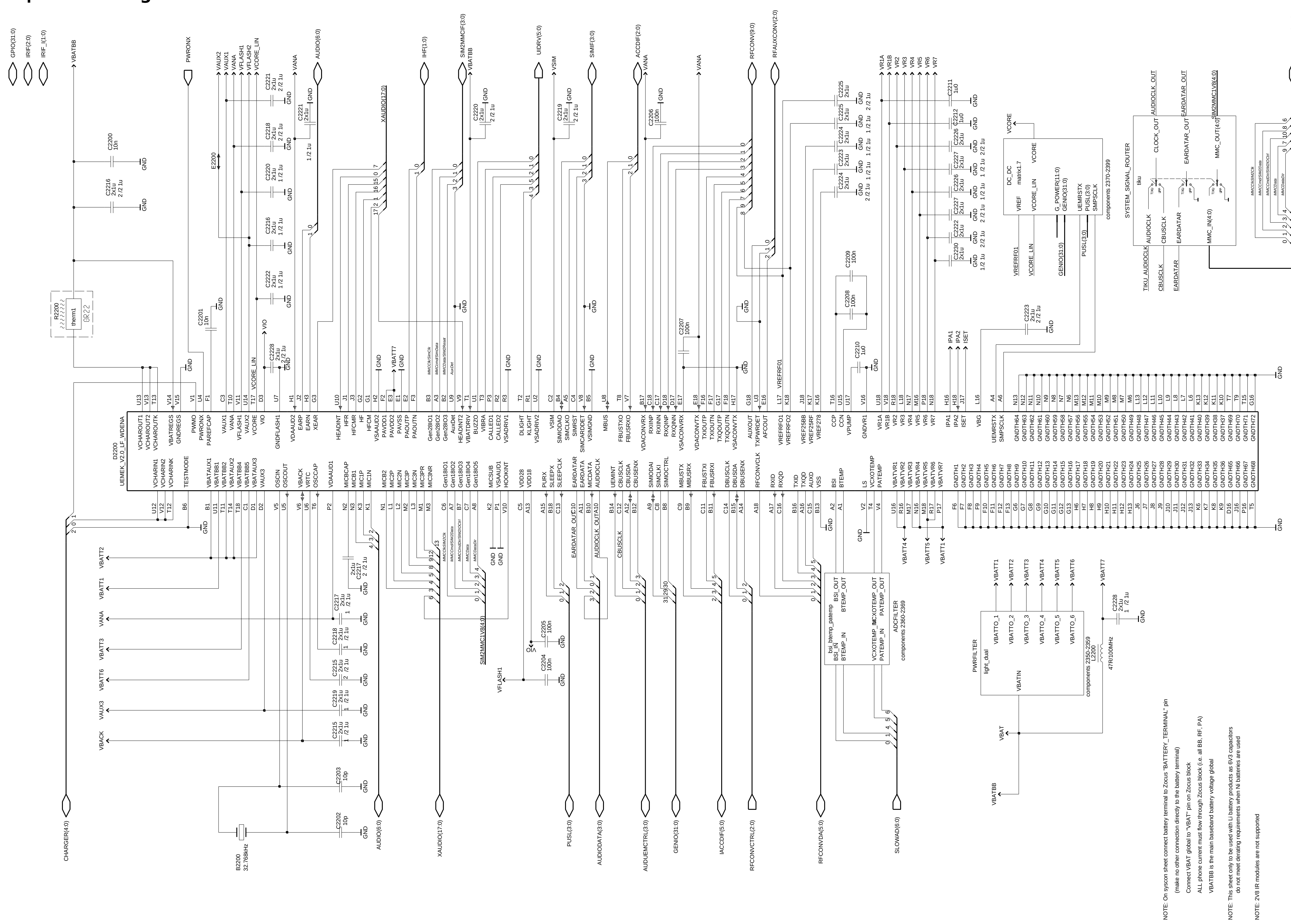
RFMD 9250 power amplifier and power detection



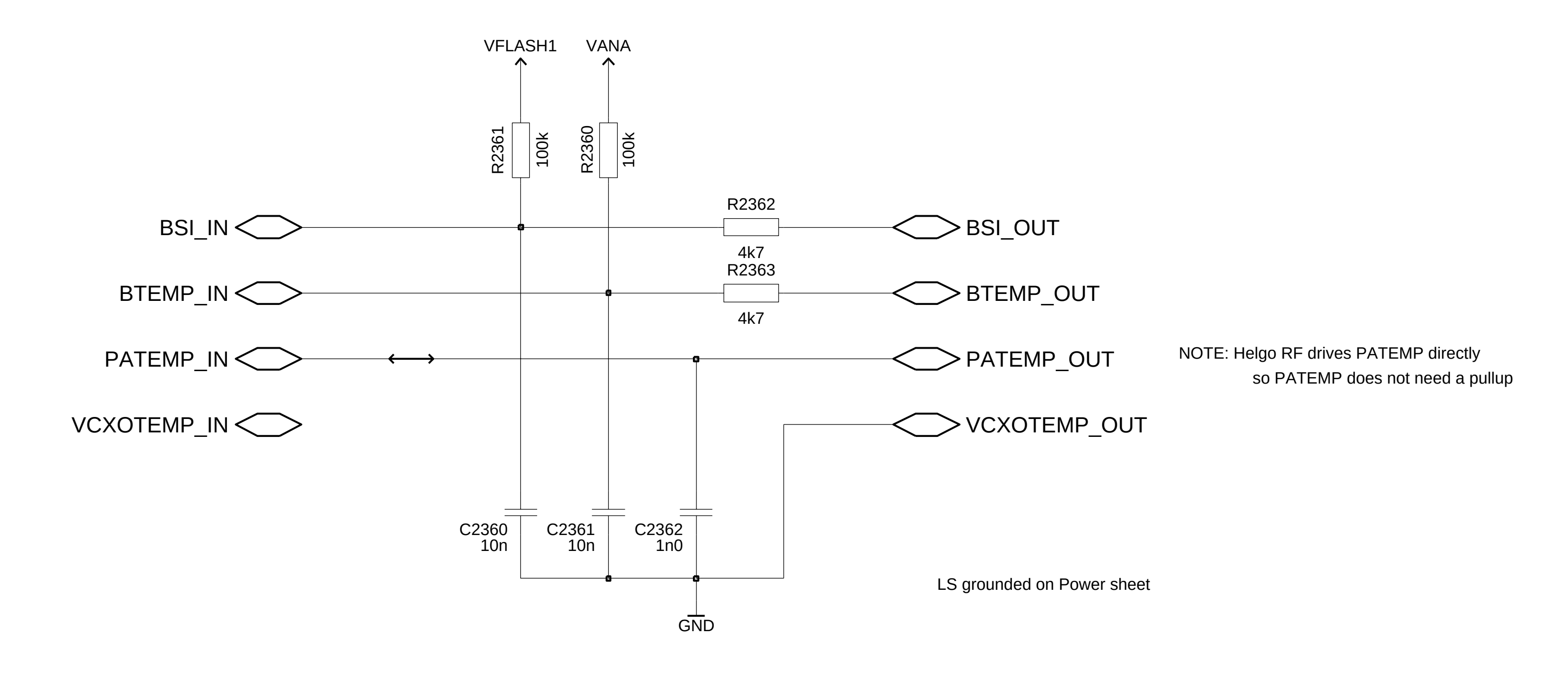
DCT45 common baseband top level



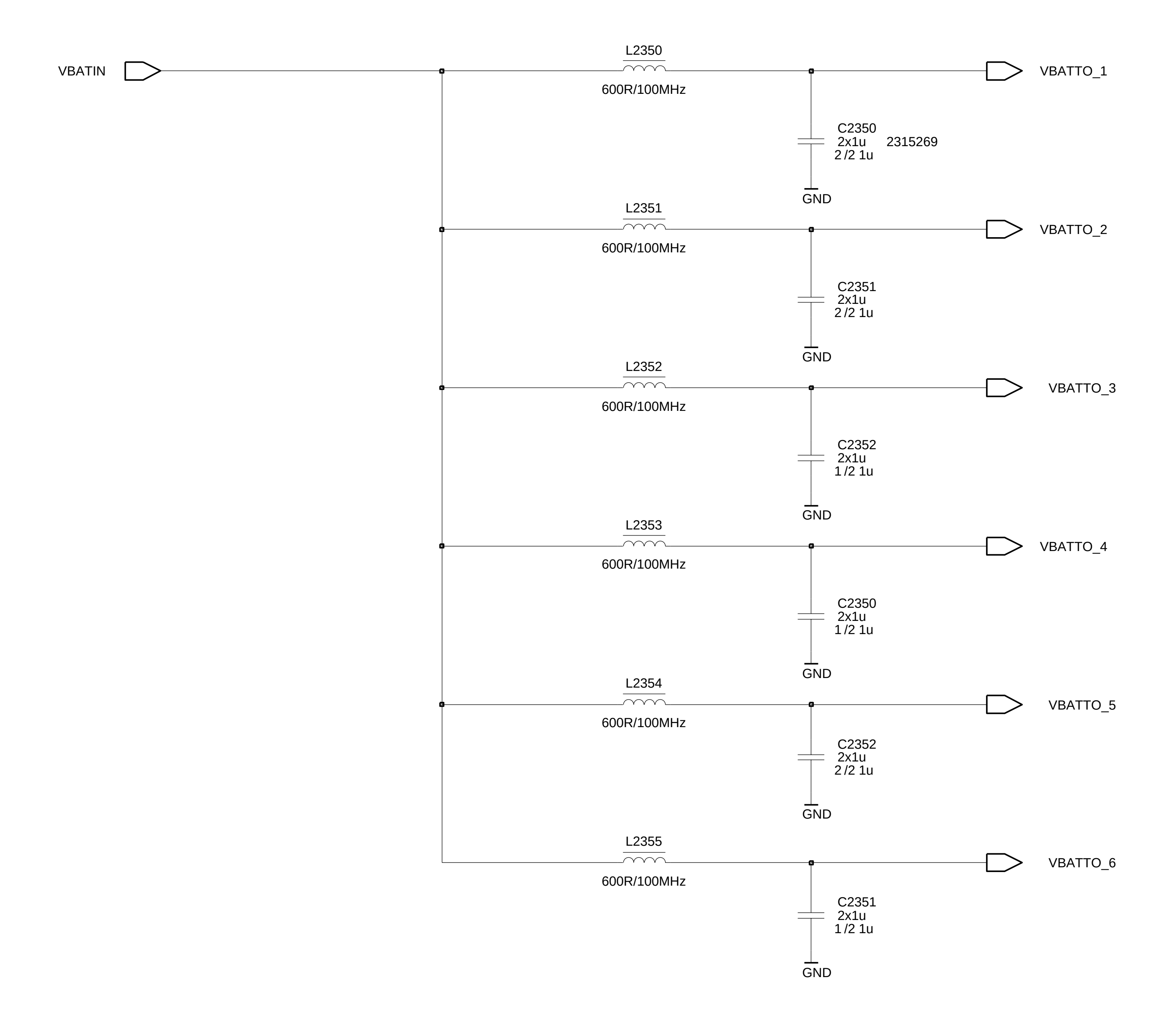
UEMEK power management



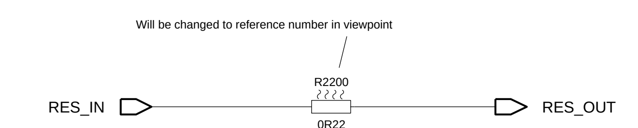
UEME ADC filter block



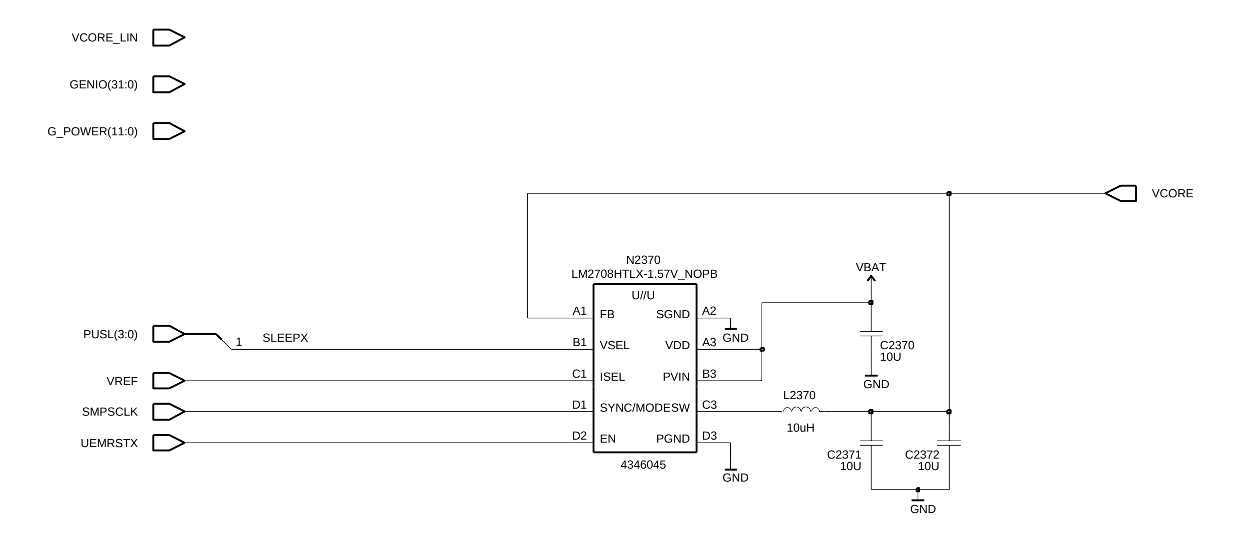
Light filtering



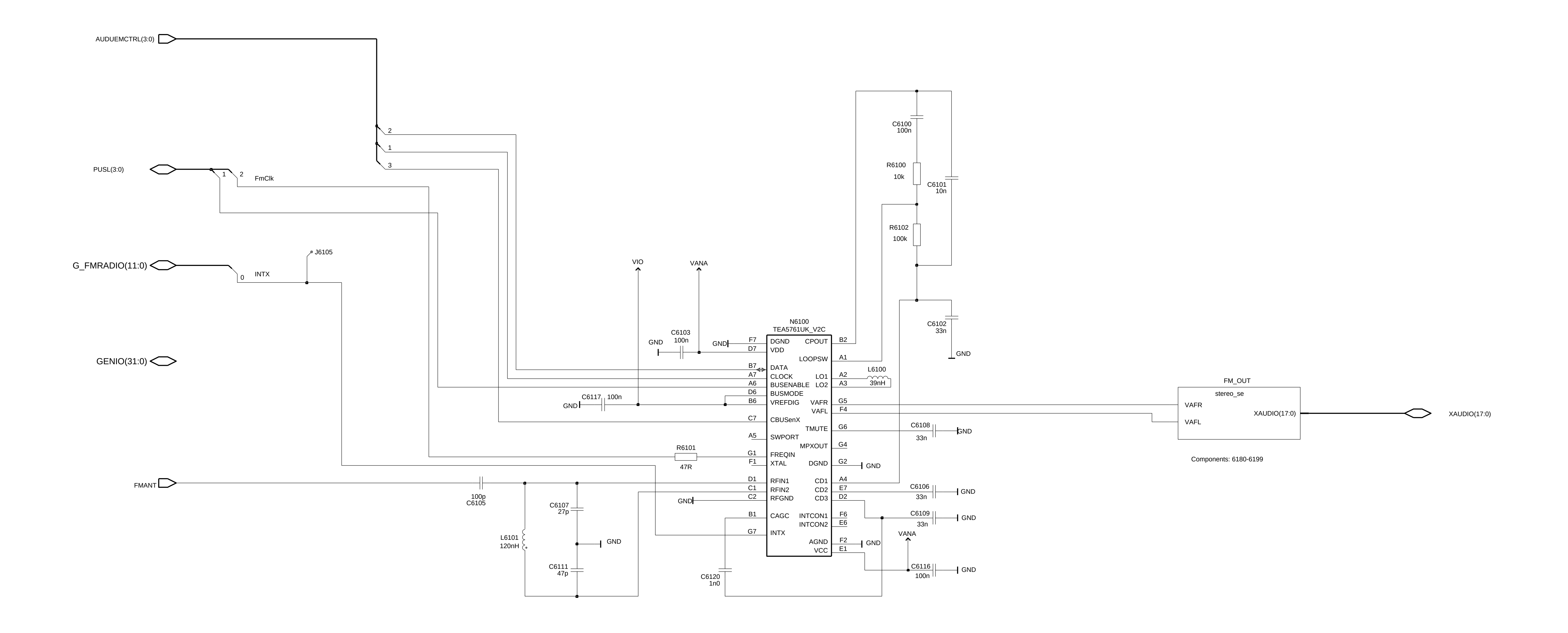
PWR resistor



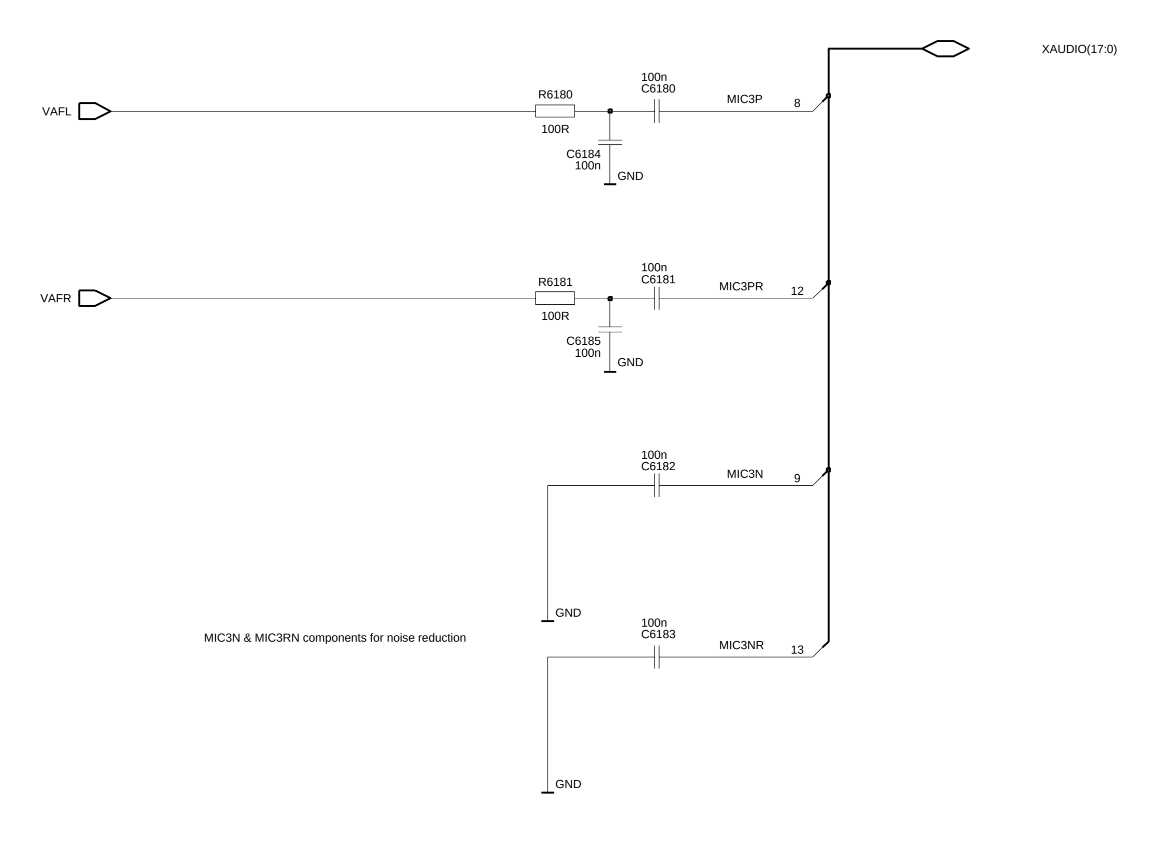
DC_DC for TIKU and VIO



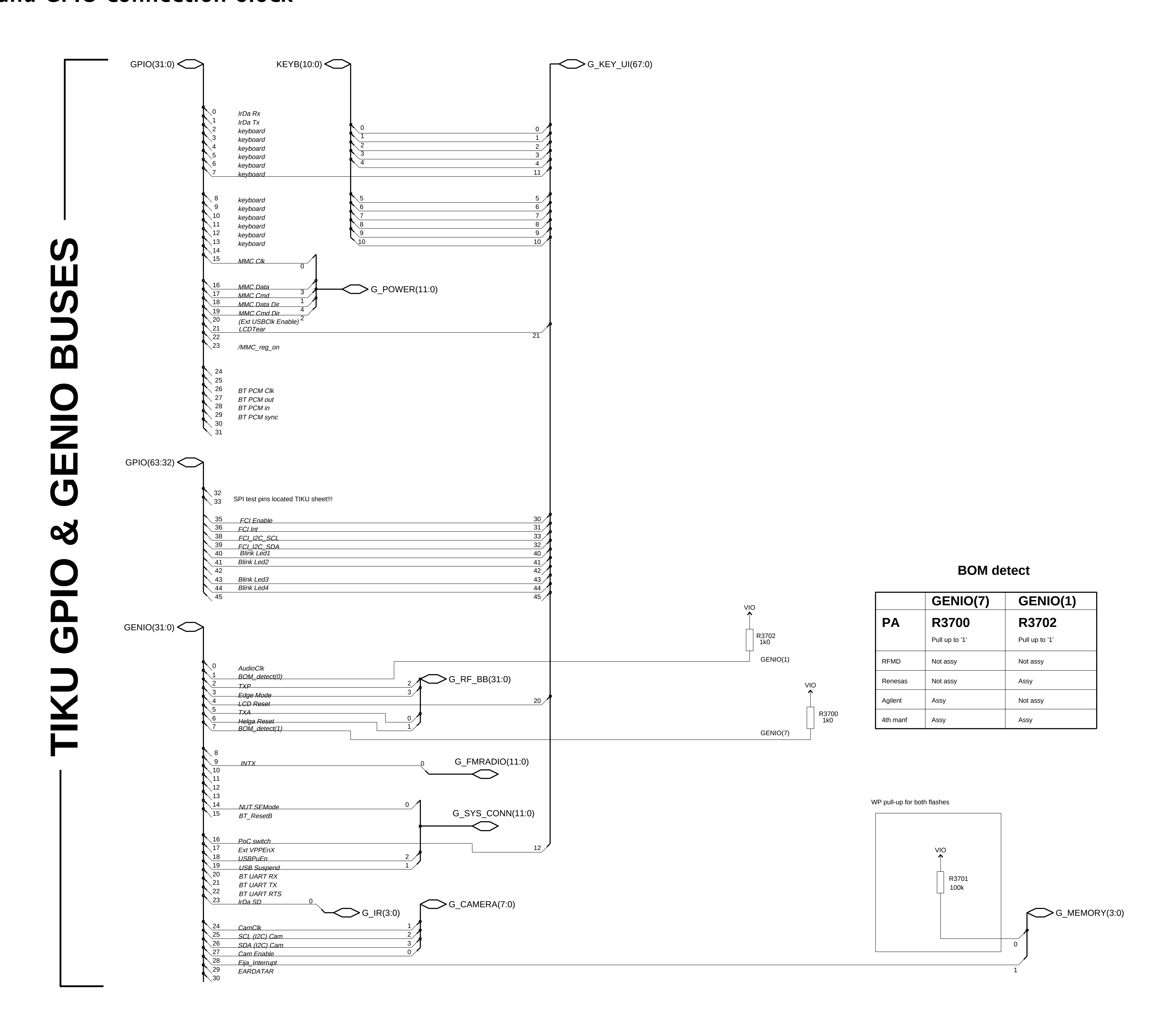
TEA 5761


AUDUEMCTRL(3:0)
PUSL(3:0)
FmClk
G_FMRADIO(11:0)
INTX
J6105
GENIO(31:0)
FMANT
VIO
VANA
N6100
TEA5761UK_V2C
DGND
VDD
DATA
CLOCK
BUSENABLE
BUSMODE
VREFDIG
CBUSenX
SWPORT
FREQIN
XTAL
RFIN1
RFIN2
RFGND
CAGC
INTX
CPOUT
LOOPSW
LO1
LO2
VAFR
VAFL
TMUTE
MPXOUT
CD1
CD2
CD3
INTCON1
INTCON2
AGND
VCC
C6100 100n
R6100 10k
C6101 10n
R6102 100k
C6102 33n
L6100 39nH
C6103 100n
C6117 100n
R6101 47R
C6108 33n
C6106 33n
C6109 33n
C6116 100n
C6105 100p
C6107 27p
C6111 47p
L6101 120nH
C6120 1n0
GND
FM_OUT
stereo_se
VAFR
VAFL
XAUDIO(17:0)
Components: 6180-6199

Differential stereo



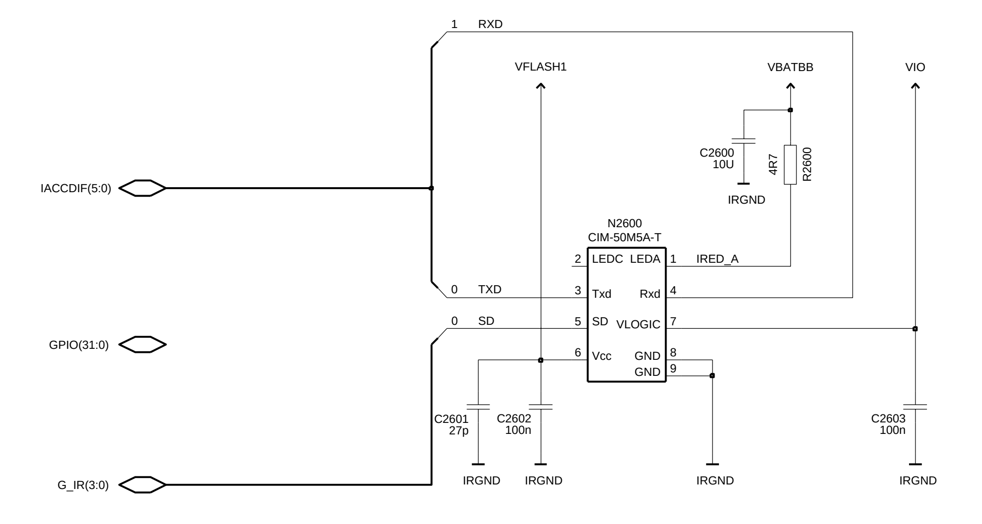
GENIO and GPIO connection block

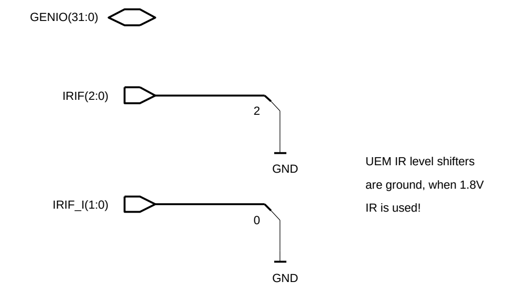


	GENIO(7)	GENIO(1)
PA	R3700 Pull up to '1'	R3702 Pull up to '1'
RFMD	Not assy	Not assy
Renesas	Not assy	Assy
Agilent	Assy	Not assy
4th manf	Assy	Assy

G_APE(15:0)
G_CDMA(11:0)
G_GPS(7:0)
G_SIM(7:0)
G_MMC(7:0)
G_LPRF(23:0)
G_AUDIO(11:0)

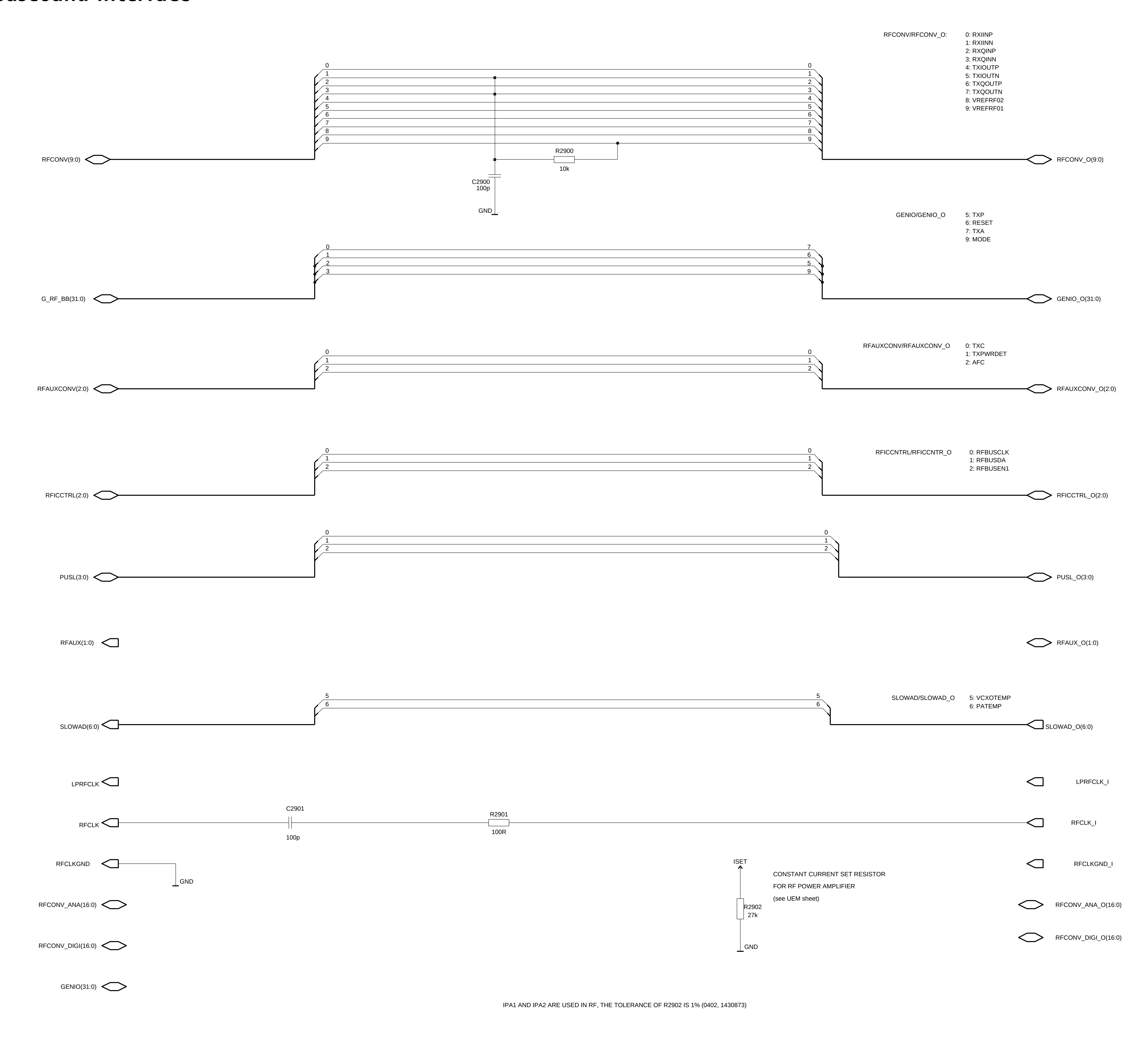
IR module


1 RXD
VFLASH1
VBATBB
VIO
C2600
10U
4R7
R2600
IRGND
IACCDIF(5:0)
N2600
CIM-50M5A-T
2 LEDC LEDA 1
IRED_A
0 TXD
3 Txd Rxd 4
0 SD
5 SD VLOGIC 7
6 Vcc GND 8
GND 9
GPIO(31:0)
C2601
27p
C2602
100n
C2603
100n
IRGND
IRGND
IRGND
IRGND
G_IR(3:0)

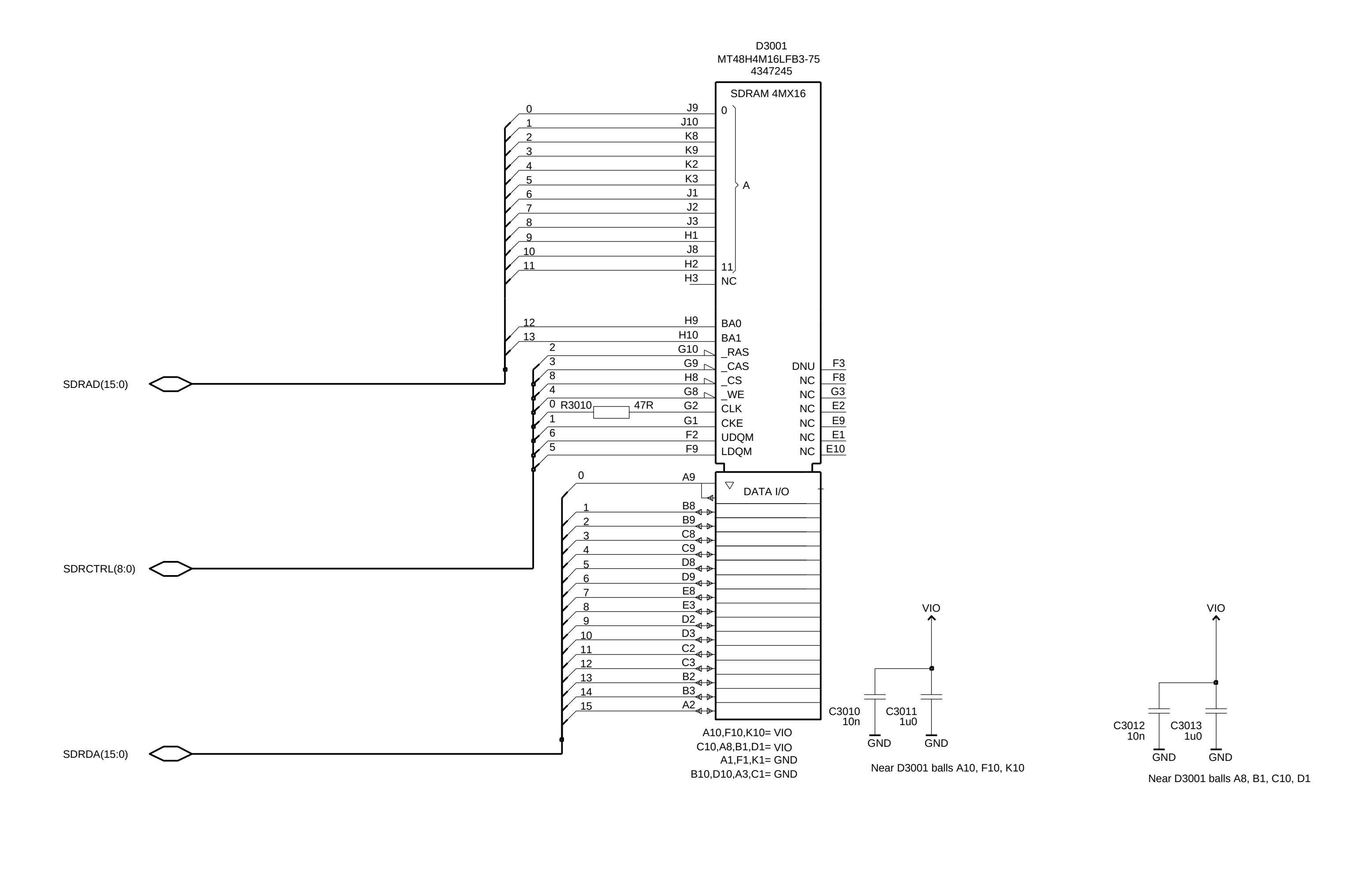

GENIO(31:0)
IRIF(2:0)
2
GND
UEM IR level shifters
are ground, when 1.8V
IR is used!
IRIF_I(1:0)
0
GND

MultiGND symbol bypass

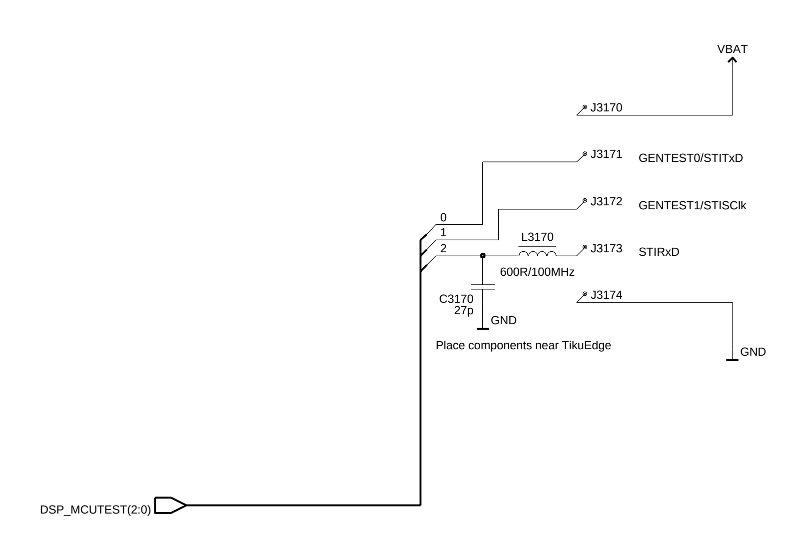
GSM RF - baseband interface



64Mbit 1v8 SDRAM memory



Testpoints based Ostrich interface



HWA



Place near TIKU terminals

VIO VIO

R3300 4k7

R3301 4k7

3 SDA

2 SCL

1 CamClk 6.5 MHz

C3303 27p

GND

G_CAMERA(7:0)

0 CamVctrl (shutdown)

EMI filter. Highly sensitive line.

C3304 10n

GND

CamVctrl

CamClk

Place near TIKU terminals

0 CCPDATA_P

CCP(4:0)

R3303 100R 1%

1 CCPDATA_N

2 CCPCLK_P

R3304 100R 1%

3 CCPCLK_N

4 CCPbus bias

GND R3305 15k

GENIO(31:0)

GPIO(31:0)

AUDUEMCTRL(3:0)

PUSL(3:0)

SDA

SCL

0

1

3 SDA

2 SCL

HWA_CAMERA(3:0)

CCPDATA_P

CCPDATA_N

CCPCLK_P

CCPCLK_N

CCPCLK_N

CCPCLK_P

3

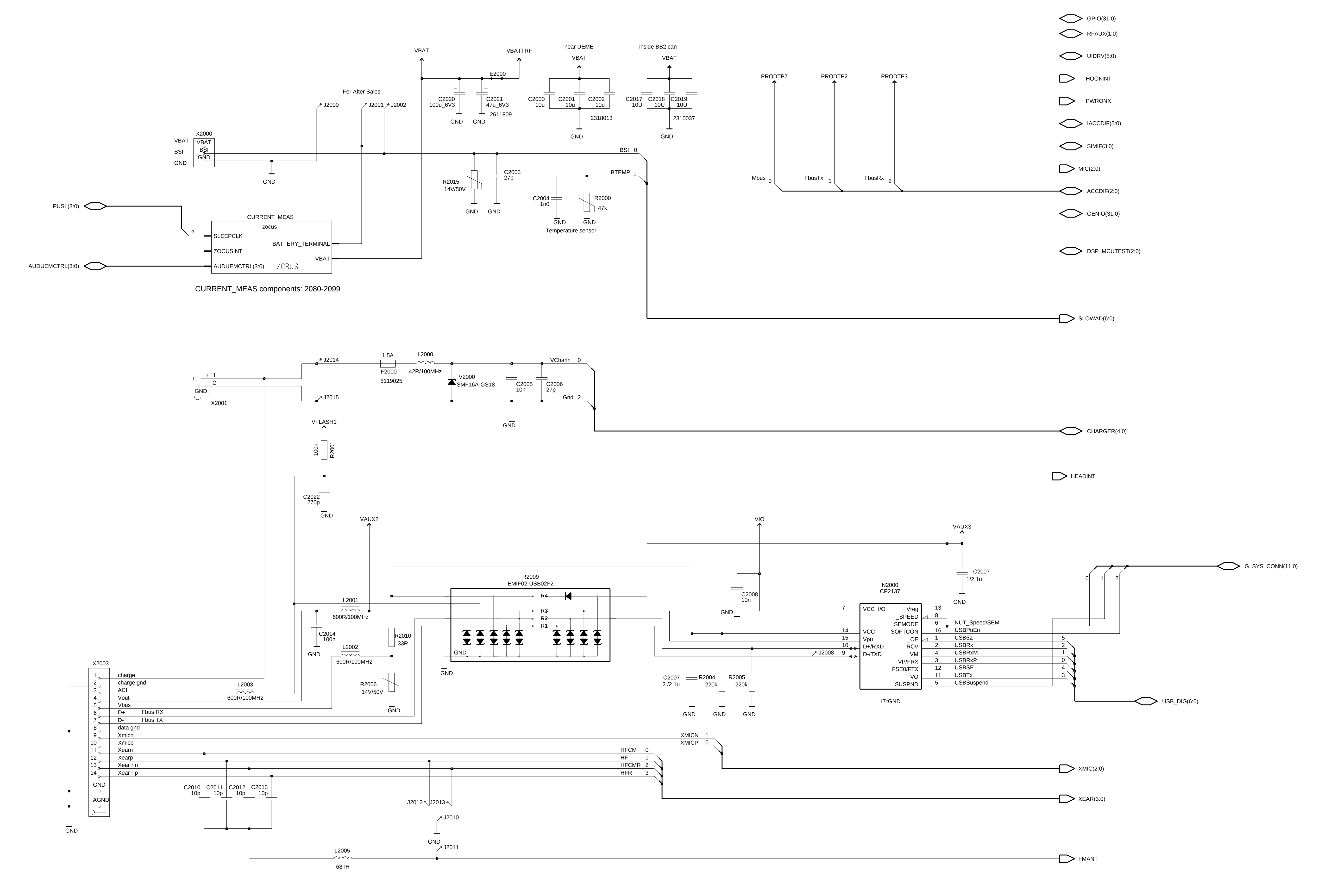
2

1 CCPDATA_N

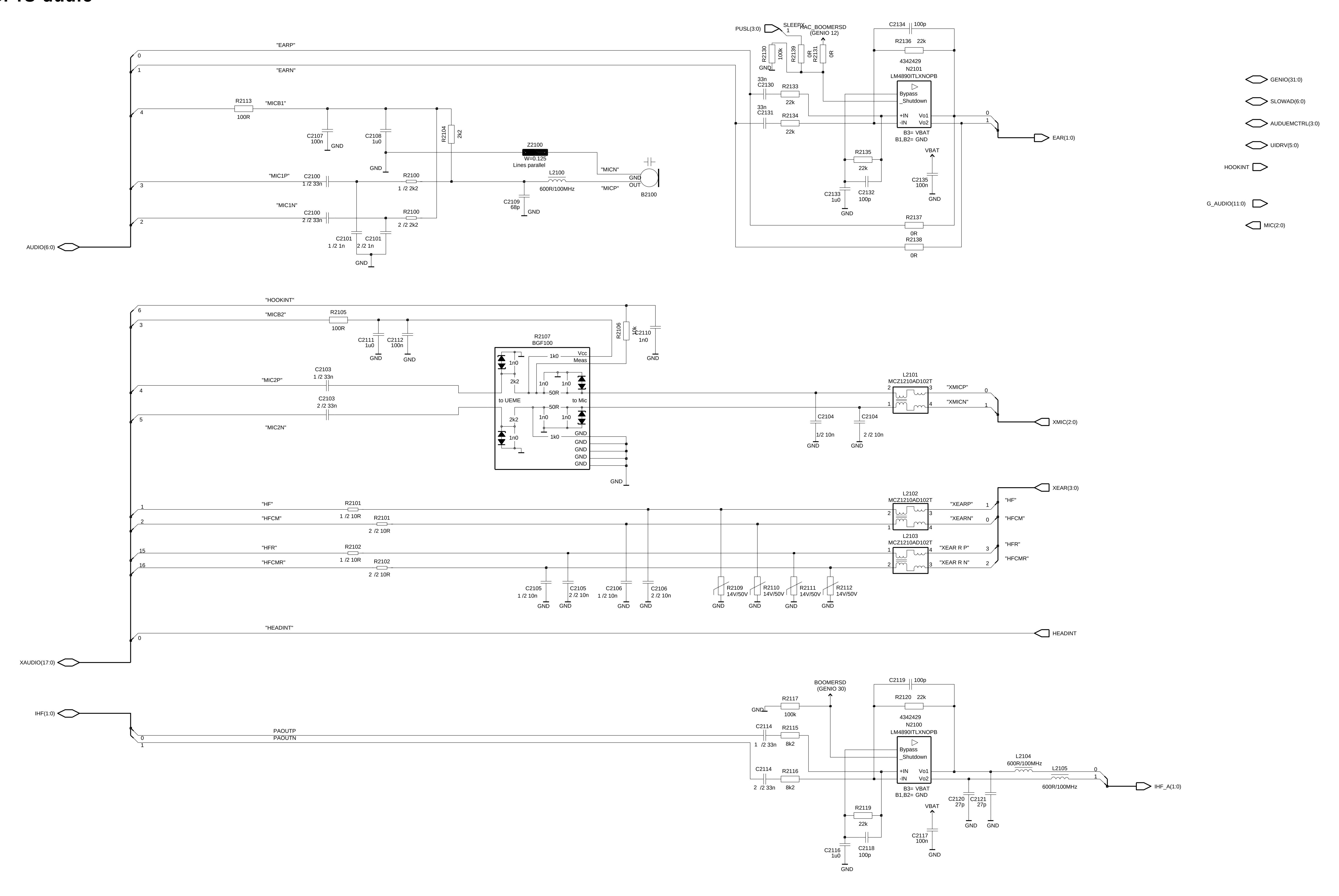
0 CCPDATA_P

HWA_CCP(3:0)

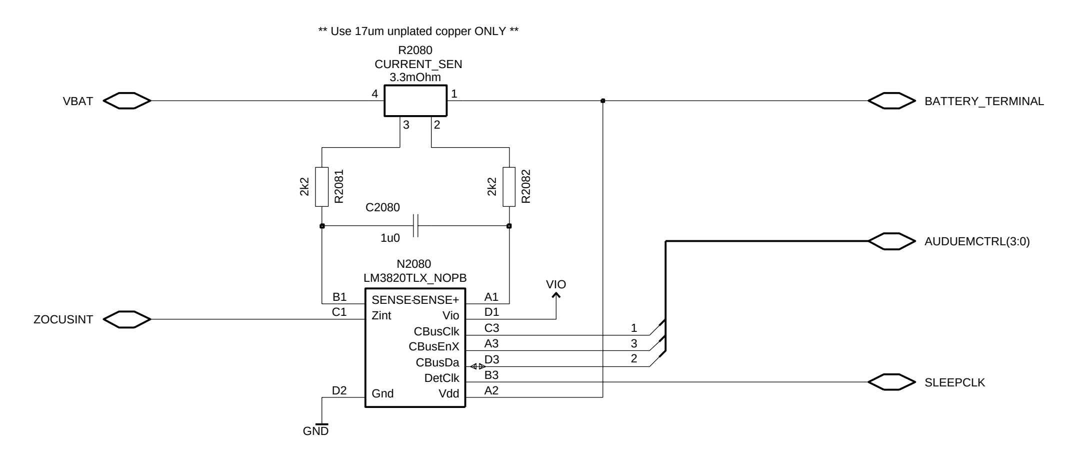
Br45 system connector



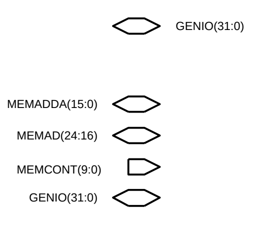
Br45 audio

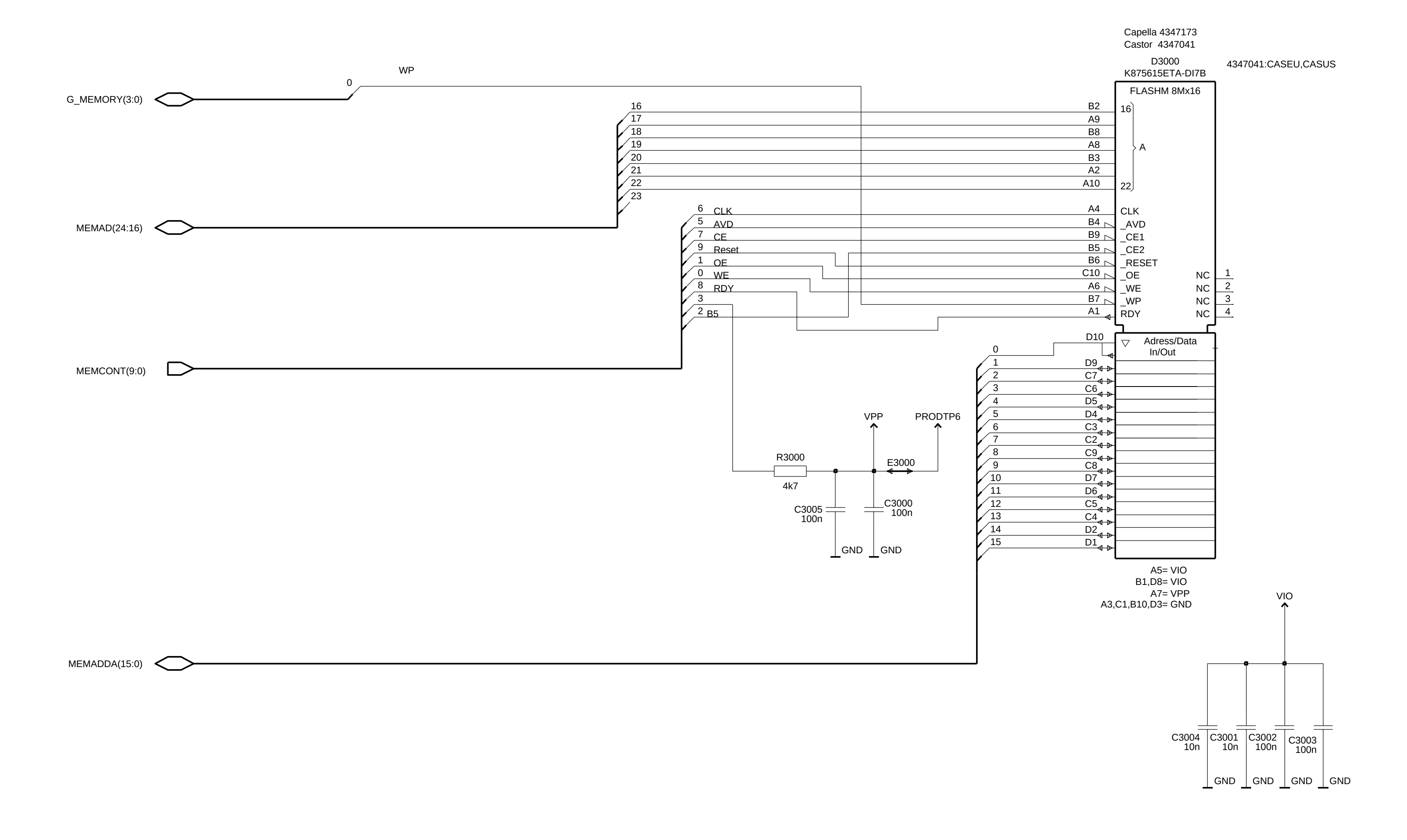


Current measure

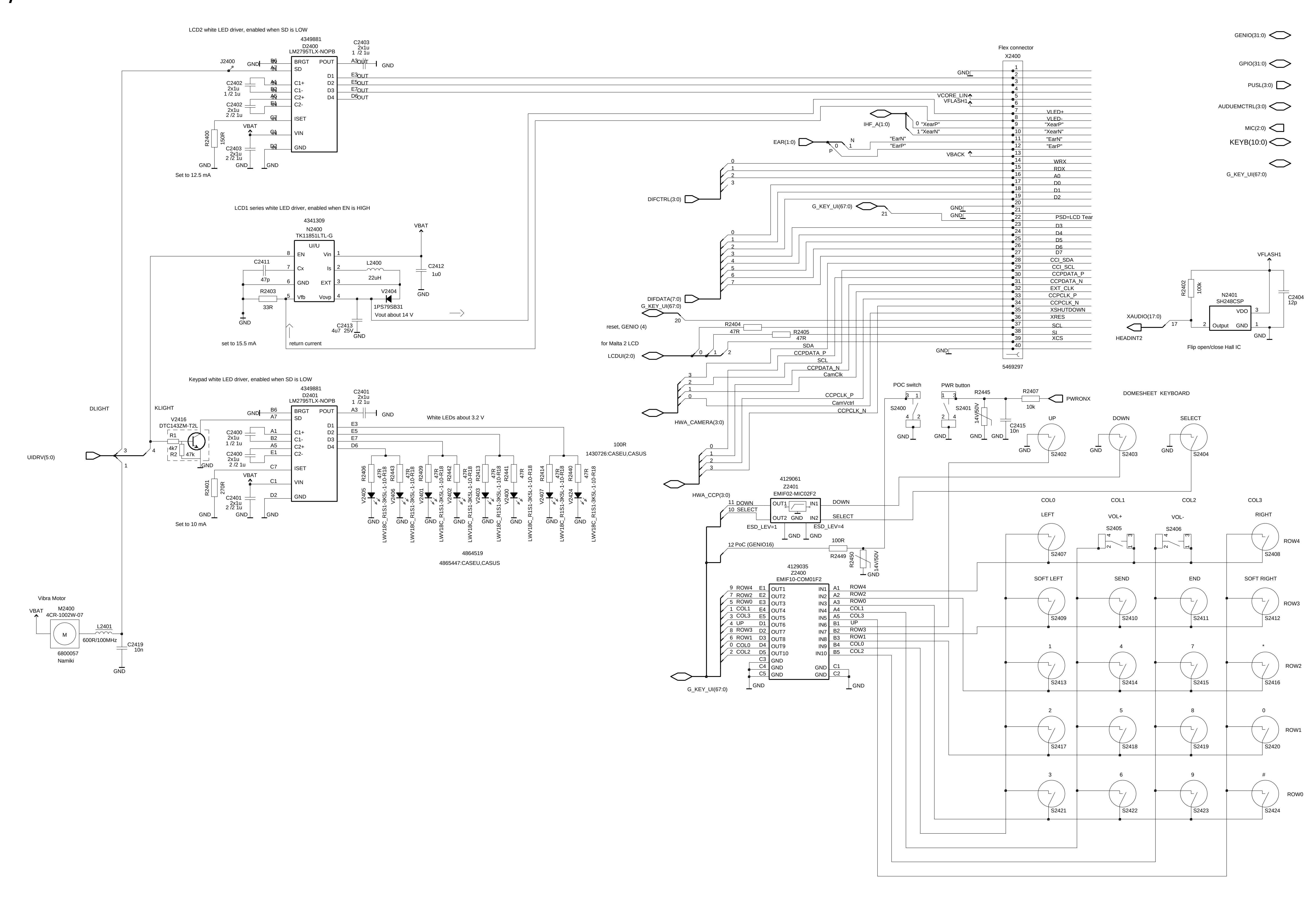


128Mbit NOR

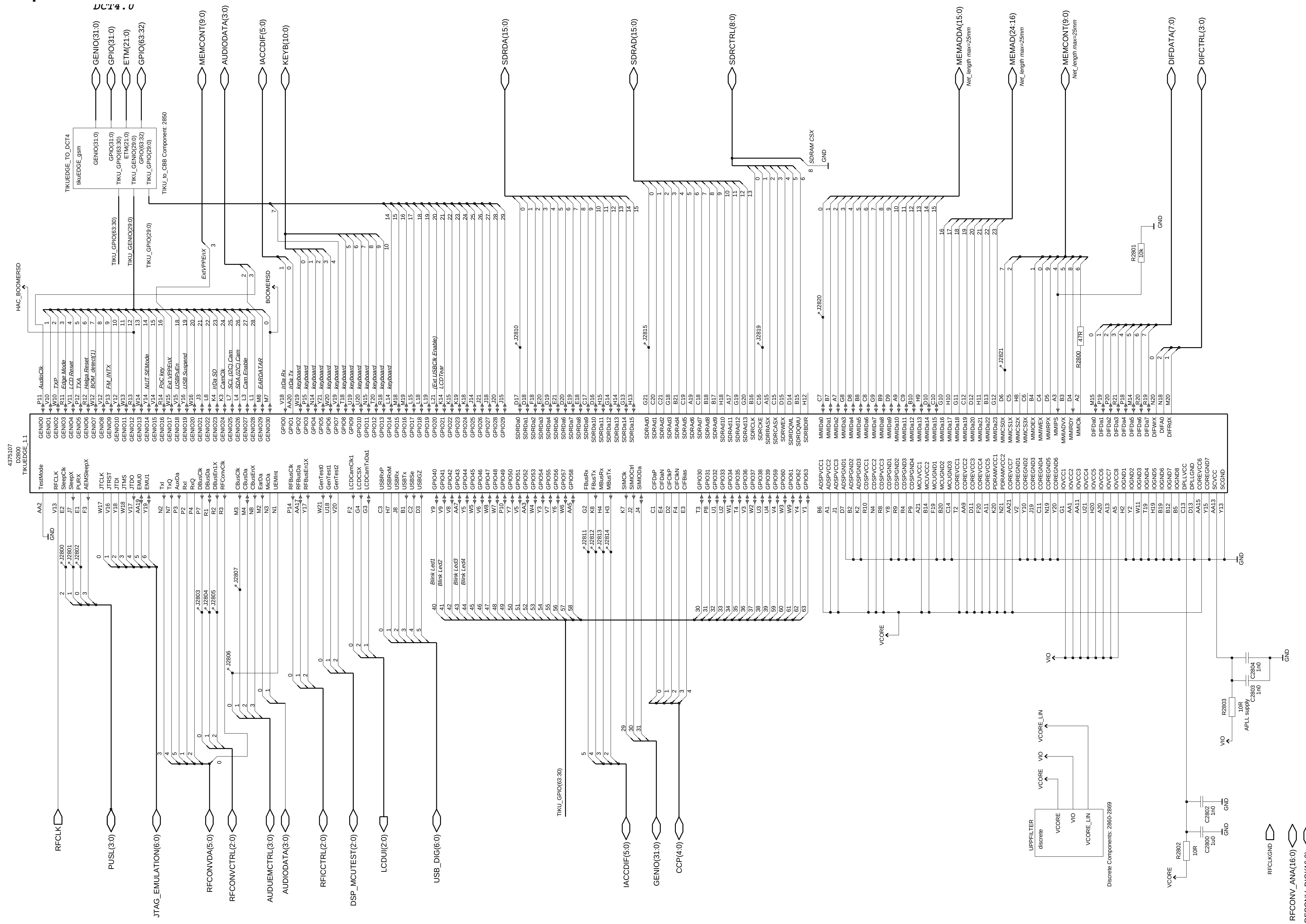

GENIO(31:0)
MEMADDA(15:0)
MEMAD(24:16)
MEMCONT(9:0)
GENIO(31:0)


Capella 4347173
Castor 4347041
D3000
K875615ETA-DI7B
4347041:CASEU,CASUS
FLASHM 8Mx16
G_MEMORY(3:0)
WP
MEMAD(24:16)
MEMCONT(9:0)
MEMADDA(15:0)
CLK
AVD
CE
Reset
OE
WE
RDY
B5
_AVD
_CE1
_CE2
_RESET
_OE
_WE
_WP
NC
Adress/Data
In/Out
VPP
PRODTP6
R3000
4k7
E3000
C3005
100n
C3000
100n
GND
A5= VIO
B1,D8= VIO
A7= VPP
A3,C1,B10,D3= GND
VIO
C3004
10n
C3001
10n
C3002
100n
C3003
100n

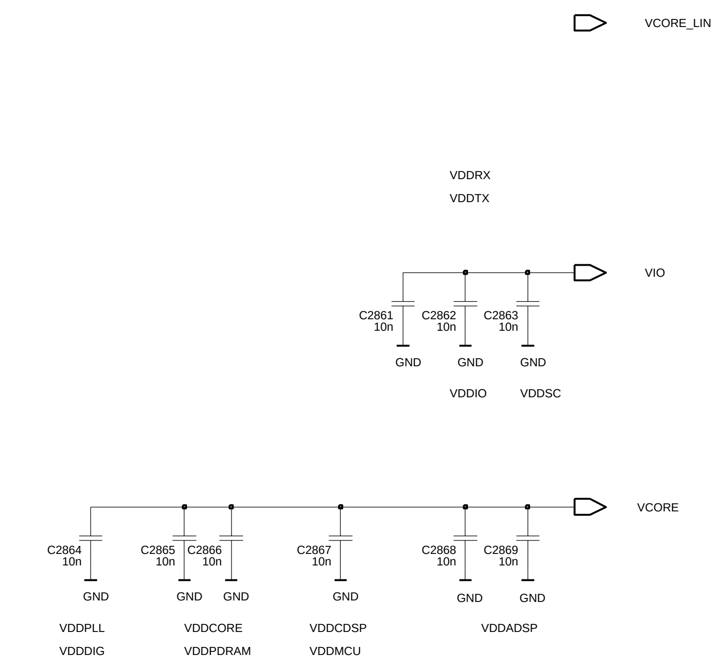
Keyboard UI



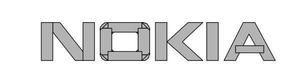
TIKU implementation



Discrete decoupling capasitors for TIKU

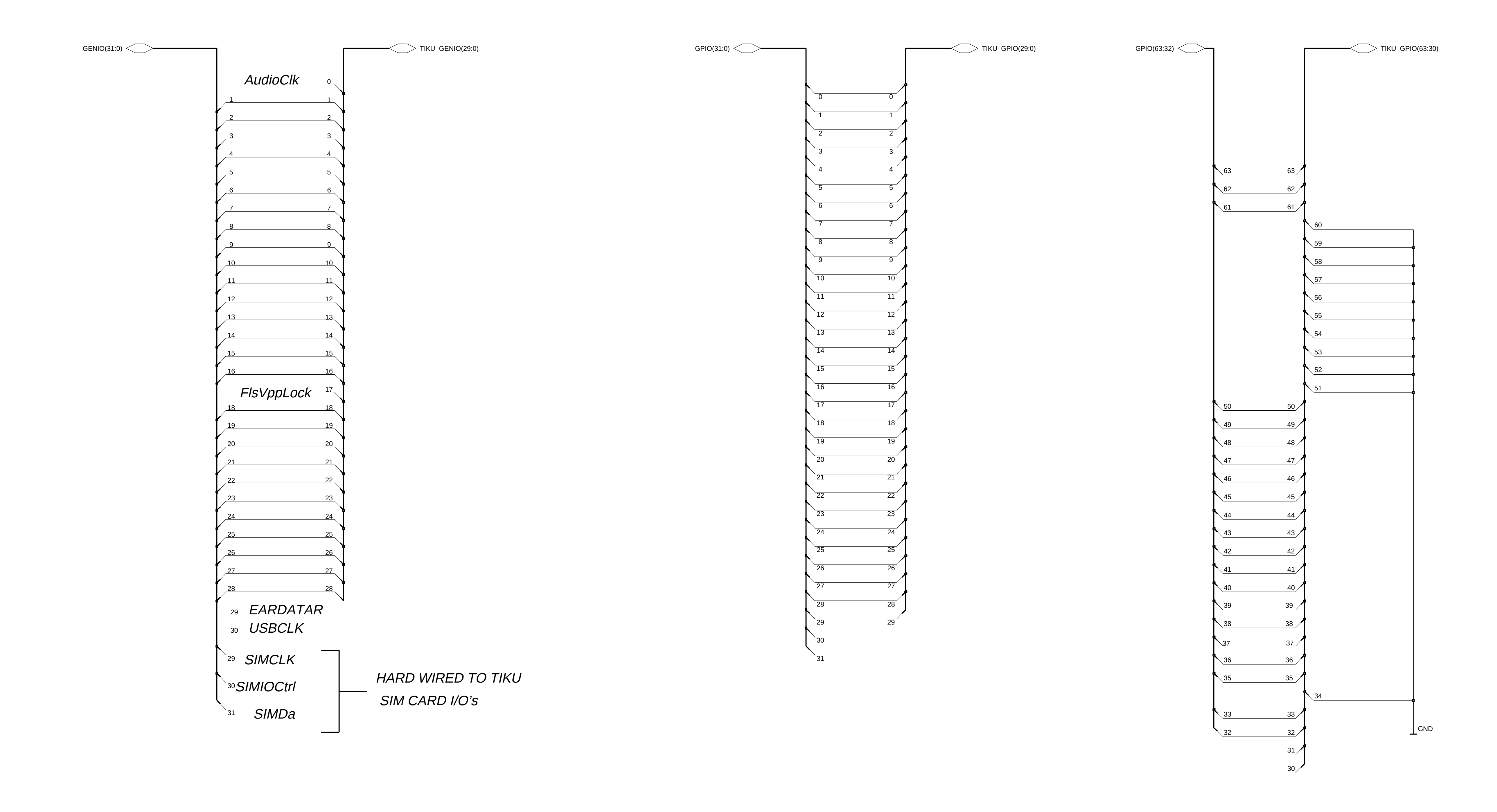


A B C D

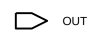


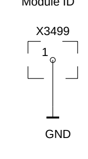
Name	Discrete decoupling capacitors for Tiku	Appr	dd-mmm-yy
Assoc	DCT4 Common Baseband	Des. M. Coombes	25-Feb-02
		Dr. AKOURI	29-Jun-04

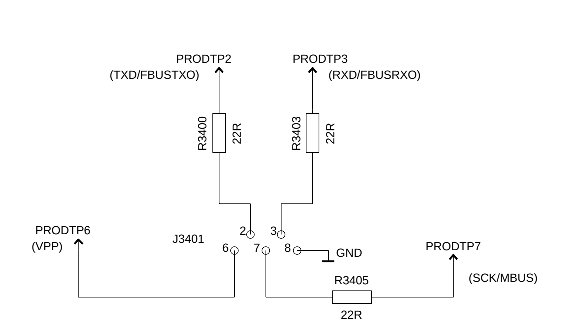
TIKU EDGE bus conversion sheet



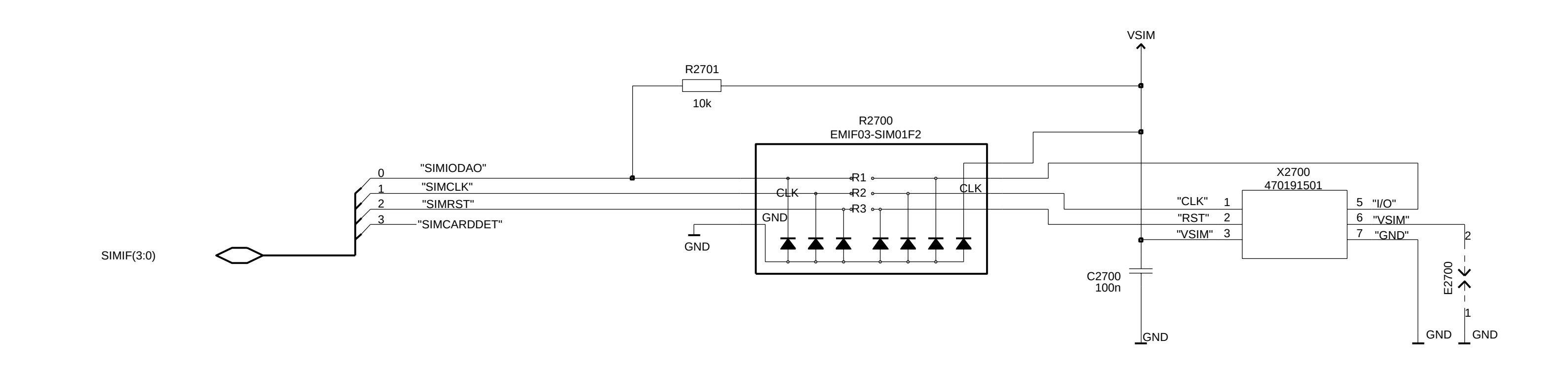
5 pine production test pattern







SIM reader

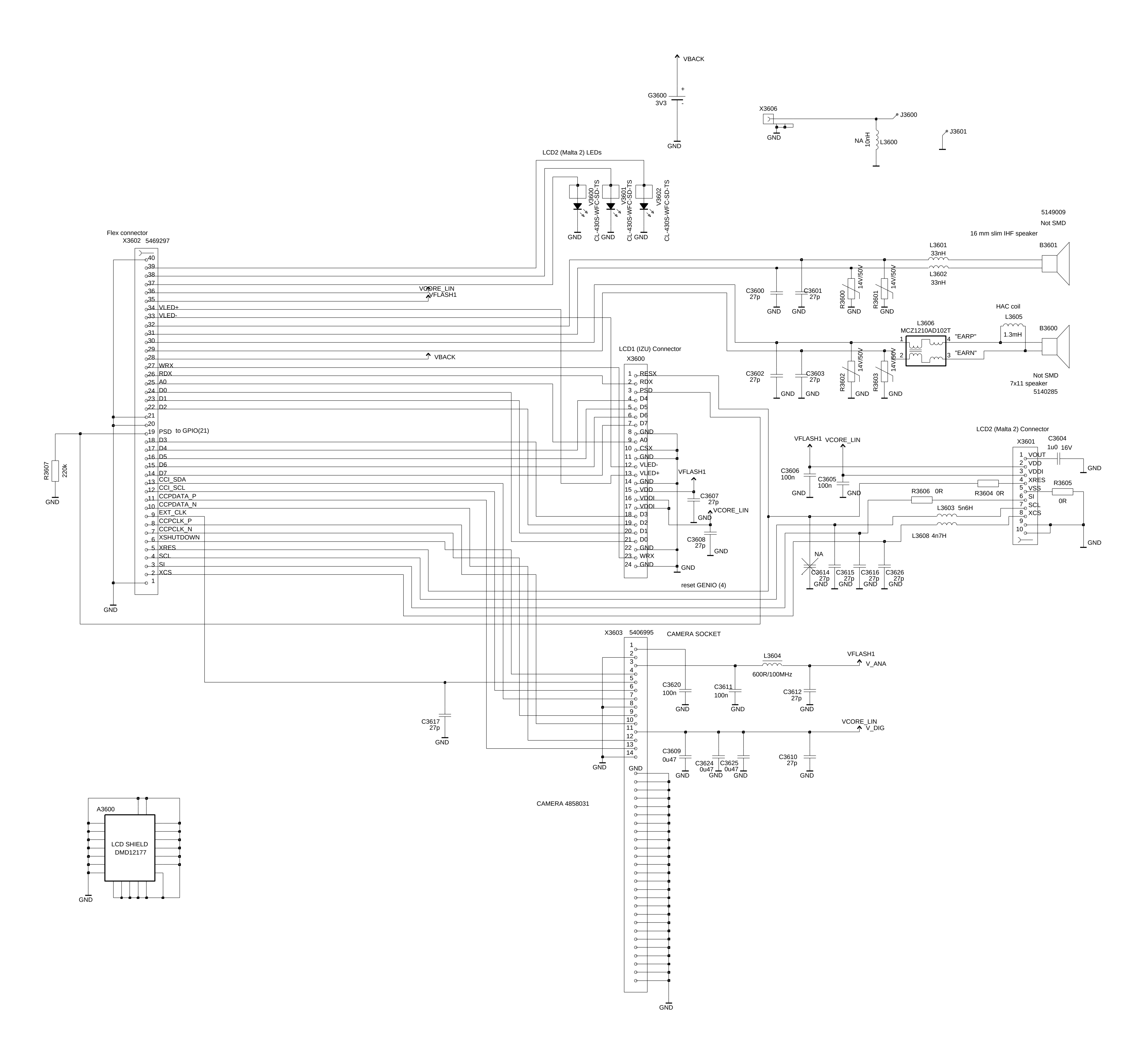


GENIO(31:0)

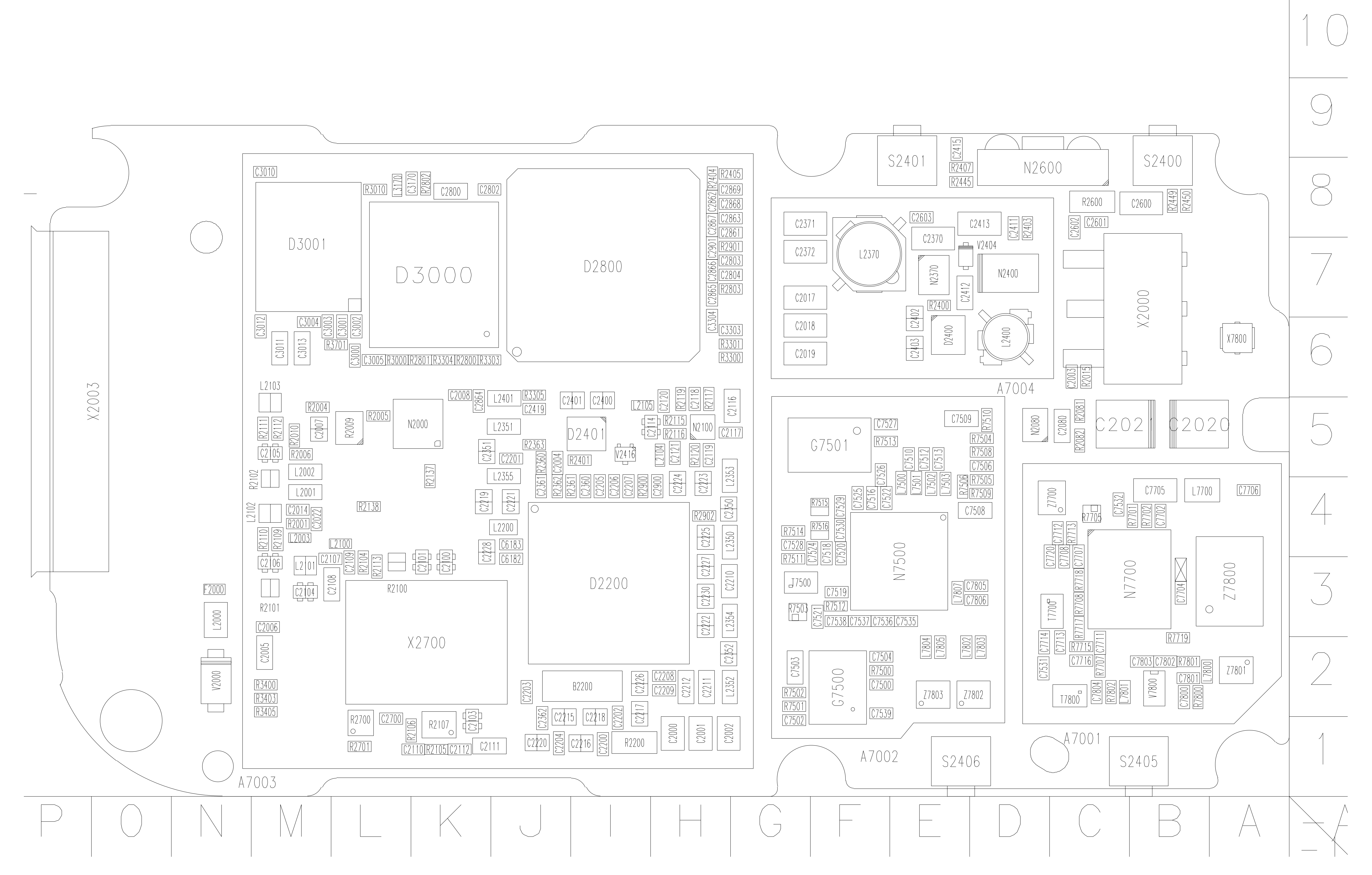
SIM2MMCIF(3:0)

G_SIM(7:0)

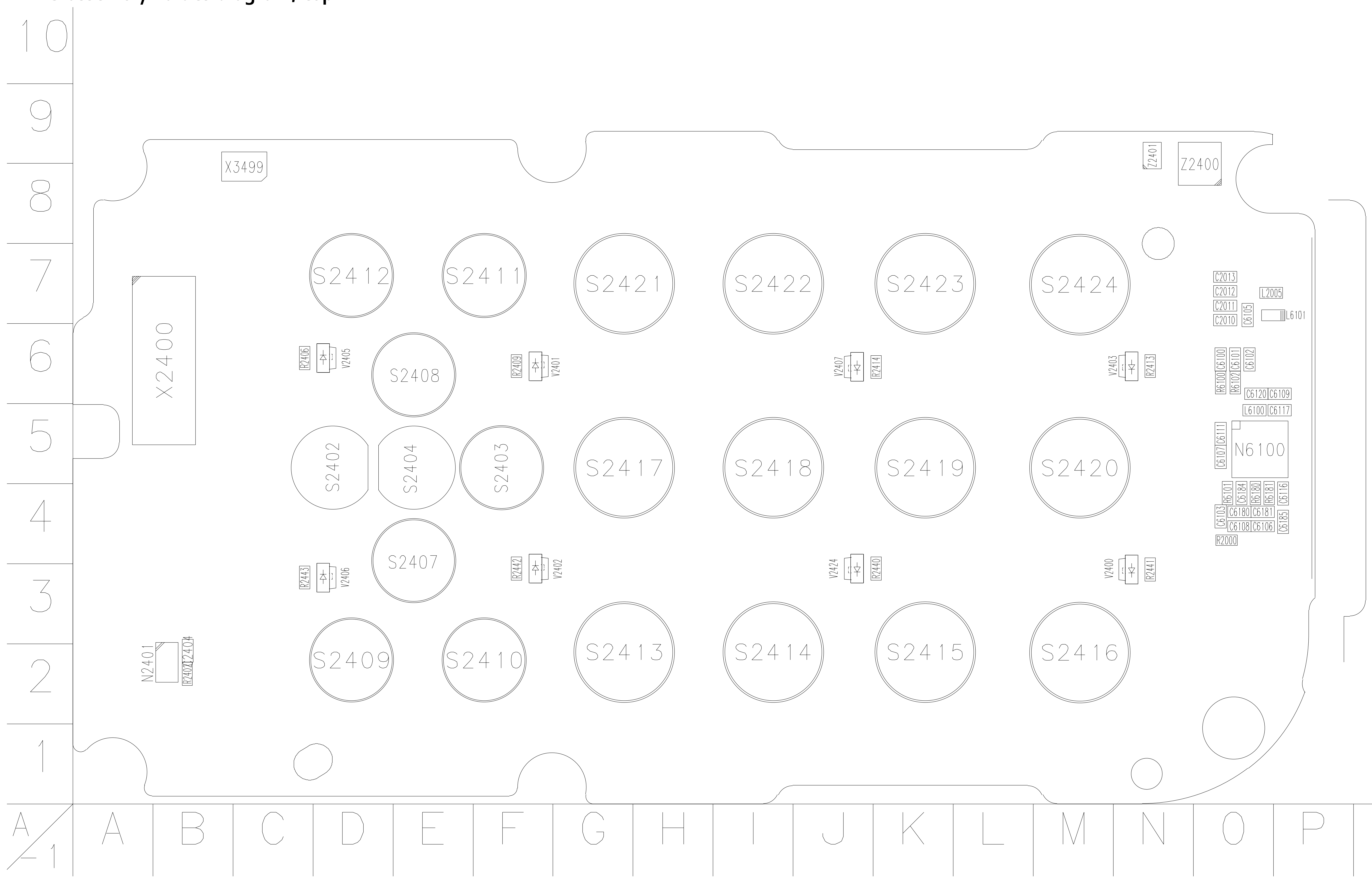
Upper block



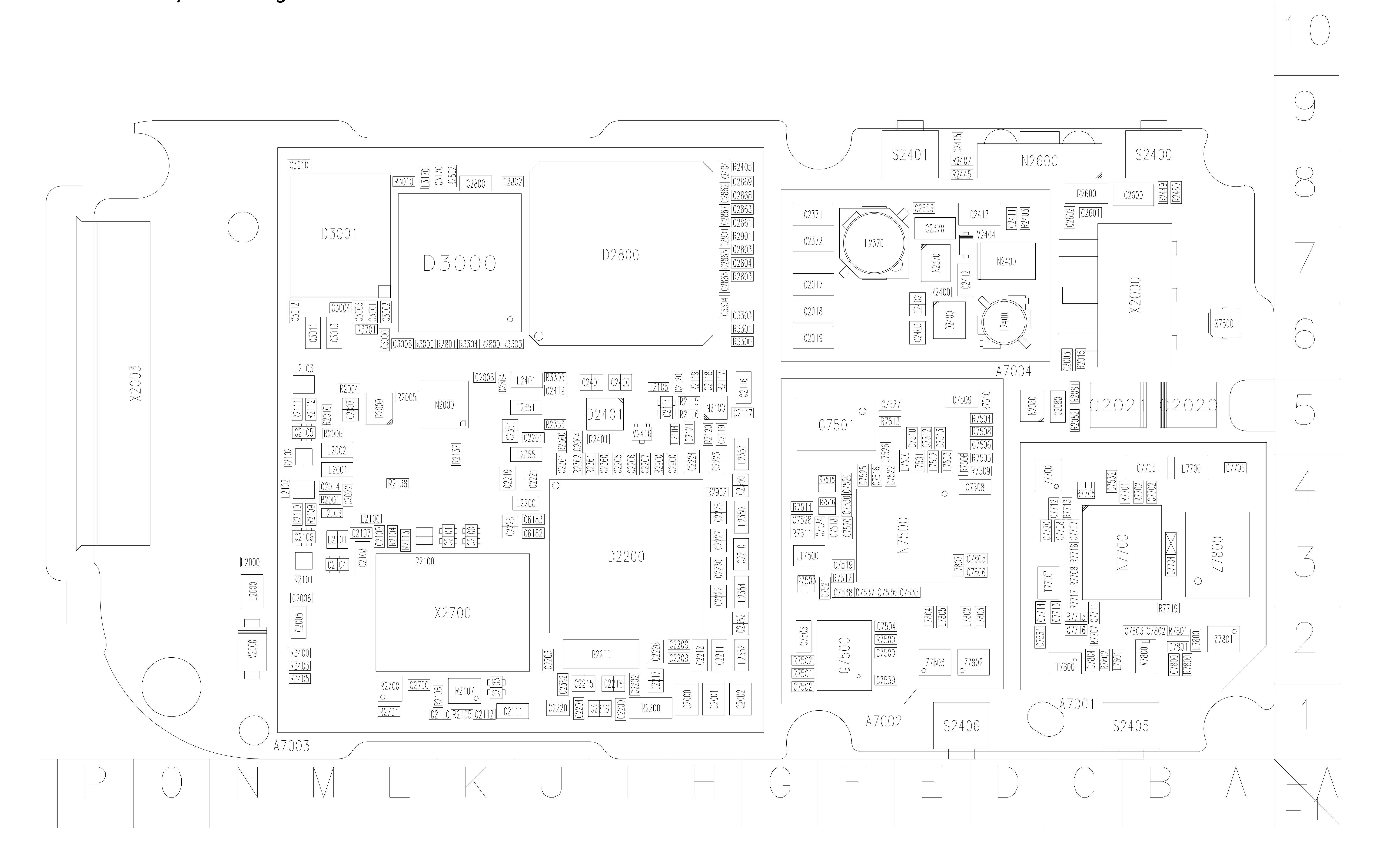
RM-8 assembly values diagram, bottom



RM-8 assembly values diagram, top



RM-47 assembly values diagram, bottom



RM-47 assembly values diagram, top

